

IN THE SPECIFICATION

Please amend the paragraph at page 9, lines 6-15, as follows:

Represented in Figure 5 is the schematic of one of these lines, comprising a correlator for the luminance and a correlator for the chrominance. These correlators essentially use logic functions of the OR, SUP and SUP/ECR type. These functions will be described subsequently in this text. This diagram also comprises D-type flip-flops ~~[[5014]]~~ 504, the well-known role of which is essentially to ensure the link between the other entities while simultaneously affording a memory effect and a delay effect so as to comply with the sequencing required for overall operation. In this schematic diagram, on each occasion just one D-type flip-flop has been represented for the understanding of the manner of operation but there will if required be the necessary number thereof in series so as to obtain proper sequencing.